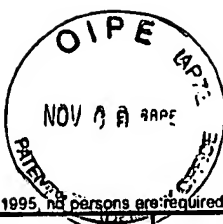
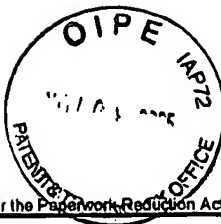


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Sheet 1

of 1

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Application Number	10/510,301
Filing Date	October 6, 2004
First Named Inventor	Ralph A. Braspenning
Art Unit	2613
Examiner Name	
Attorney Docket Number	NL 020285

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/A.H./	1	KIM JONG WON ET AL; "HIERARCHICAL VARIABLE BLOCK SIZE MOTION ESTIMATION TECHNIQUE FOR MOTION SEQUENCE CODING", OPTICAL ENGINEERING, SOC. OF PHOTO-OPTICAL INSTRUMENTATION	
		ENGINEERS, BELLINGHAM, US., VOL. 33, NO. 8. 08/01/1994; PAGES 2553-2561; XP000462269.	
/A.H./	2	L. VANDENDORPE ET AL; "MOTION-COMPENSATED CONVERSION FROM INTERLACED TO PROGRESSIVE FORMATS", SIGNAL PROCESSING, IMAGE COMM. ELSEVIER SCIENCE PUBLISHERS, AMSTERDAM, NL,	
		VO. 6, NO. 3 06/01/1994; PAGES 193-211; XP000451925.	
/A.H./	3	LEE JAEHUN ET AL; "A NEW VLSI ARCHITECTURE OF A HIERARCHICAL MOTION ESTIMATOR FOR LOW BIT-RATE VIDEO CODING", IMAGE PROCESSING, ICIP 99, PROCEEDINGS,	
		99 INTERNATIONAL CONFERENCE ON KOBE, JAPAN 10/24/1999, PISCATAWAY, NJ, PAGES 774-778, XP010369030.	

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